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MIDTERM EXAMINATION
Spring 2009
CS401- Computer Architecture and Assembly Language Programming (Session - 2)

Question No: 1 (Marks: 1) - Please choose one

To transfer control back the RET instruction take

► 1 argument

► 1 argument

► 3 arguments

► No arguments

Question No: 2 (Marks: 1) - Please choose one

In STOSB instruction SI is decremented or incremented by

► 4

► 1

► 2

► 3

Question No: 3 (Marks: 1) - Please choose one

CMPS instruction subtracts the source location to the destination location.
Destination location always lies in

► DS:SI

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► DS:DI

► ES:SI

► ES:DI

Question No: 4 (Marks: 1) - Please choose one

Regarding assembler, which statement is true:

► Assembler converts mnemonics to the corresponding OPCODE

► Assembler converts OPCODE to the corresponding mnemonics

► Assembler executes the assembly code all at once

► Assembler executes the assembly code step by step

Question No: 5 (Marks: 1) - Please choose one

If “BB” is the OPCODE of the instruction which states to “move a constant value to AX register”, the hexadecimal representation (Using little Endian notation) of the instruction “Mov AX,336” (“150” in hexadecimal number system) will be:

► 0xBB0150

► 0x5001BB

► 0x01BB50

► 0xBB5001

Question No: 6 (Marks: 1) - Please choose one

In the instruction MOV AX, 5 the number of operands are

► 1

► 2

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► 3

► 4

Question No: 7 (Marks: 1) - Please choose one

The maximum parameters a subroutine can receive (with the help of registers) are

► 6

► 7

► 8

► 9

Question No: 8 (Marks: 1) - Please choose one

In assembly the CX register is used normally as a _____register.

► source

► counter

► index

► pointer

Question No: 9 (Marks: 1) - Please choose one

All the addressing mechanisms in iAPX88 return a number called _____ *address*.

► effective

► faulty

► indirect

► direct

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Question No: 10 (Marks: 1) - Please choose one

When a 16 bit number is divided by an 8 bit number, the dividend will be in

- ► AX
- ► BX
- ► CX
- ► DX

Question No: 11 (Marks: 1) - Please choose one

in Left-Shift-Operation the left most bit _____

- ► will drop
- ► will go into CF
- ► Will come to the right most
- ► will be always 1

Question No: 12 (Marks: 1) - Please choose one

Suppose the decimal number "35" after shifting its binary two bits to left, the new value becomes _____

- ► 35
- ► 70
- ► 140
- ► 17

Question No: 13 (Marks: 1) - Please choose one

When divide overflow occurs processor will be interrupted this type of interrupt is called

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► Hardware interrupt

► Software interrupt

► Processor exception

► Logical interrupts

Question No: 14 (Marks: 1) - Please choose one

Which mathematical operation is dominant during the execution of SCAS instruction

► Division

► Multiplication

► Addition

► Subtraction

Question No: 15 (Marks: 1) - Please choose one

After the execution of REP instruction CX will be decremented then which of the following flags will be affected?

► CF

► OF

► DF

► No flags will be affected

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Question No: 16 (Marks: 1) - Please choose one

_____ is one of the reasons due to which string instructions are used in 8088

- ► Efficiency and accuracy
- ► Reduction in code size and accuracy
- ► Reduction in code size and speed
- ► Reduction in code size and efficiency

Question No: 17 (Marks: 1)

Write any two control instructions.

Question No: 18 (Marks: 1)

RET instruction take how many arguments

Question No: 19 (Marks: 2)

Explain the fuction of rotate right (ROR) instruction

Question No: 20 (Marks: 2)

Describe the PUSH function

Question No: 21 (Marks: 3)

Write down the names of four segment registers?

Question No: 22 (Marks: 3)

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For what purpose "**INT 4**" is reserved?

Question No: 23 (Marks: 5)

Given that [BX+0x0100] BX=0x0100
Ds=0xFFF0
Calculate the physical addres

Question No: 24 (Marks: 5)

Explain CMPS instruction

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