CS401 Final term 11-02-2011
Solved
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Question No: 01 (Marks: 1) - Please choose one
In a video memory, each screen location corresponds to
- One byte
- **Two bytes**
- Four bytes
- Eight bytes

Question No:02 6 (Marks: 1) - Please
INT 10 is used for……services.
- . RAM
- . Disk
- . BIOS video
- **. DOS video**

Question No: 03 (Marks: 1) - Please choose one
In graphics mode a location in video memory corresponds to a _____ on the screen.
- ► **line**
- ► **dot**
- ► **circle**
- ► **rectangle**

Question No: 04(Marks: 1)
- Please choose one
IRQ 0 interrupt have _____ priority
- low
- medium
- **highest**
- lowest

Question No: 05(Marks: 1)
In DOS input buffer , number of characters actually read on return is stored in
 First byte
Second byte
 Third byte
 Fourth byte

Question No: 06(Marks: 1)
VESA VEB 2.0 is standard for
High Resolution Mode
 Low Resolution Mode
 Very High Resolution Mode
 Medium Resolution Mode

Question No: 07(Marks: 1)
Thread registration code initialize PCB and add to linked list so that _____ will give it turn.

 Assembler
 Linker
Scheduler
 Debugger

Question No: 08 (Marks: 1) - Please choose one

Which of the following gives the more logical view of the storage medium
► BIOS
► DOS
► Both
► None
Question No: 09(Marks: 1)
IRQ 0 has priority
 Low
 High
Highest
 Medium
Question No: 10 (Marks: 1) - Please choose one
Which of the following IRQs is used for Floppy disk derive?
► IRQ 4
► IRQ 5
► IRQ 6
► IRQ 7
Question No: 11(Marks: 1)
IRQ for sound card is _____***5***_____
Question No: 12 (Marks: 1) - Please choose one
In 68K processors there is a ..... program counter (PC) that holds the address of currently executing instruction
► 8bit
► 16bit
► 32bit
► 64bit
Question No: 13 (Marks: 1) - Please choose one
In 68K processors there is a **32bit**.---------- that holds the address of currently executing instruction
- **PC**
- RET
- IDT
- IVT

Question No: 14 (Marks: 1) - Please choose one
In NASM an imported symbol is declared with the ..... while and exported symbol is declared with the .....
► Global directive, External directive
► External directive, Global directive
► Home Directive, Foreign Directive

► Foreign Directive, Home Directive

Question No: 15 (Marks: 1) - Please choose one

In assembly the CX register is used normally as a _____register.

- source
- **counter**
- index
- pointer

Question No: 16 (Marks: 1) - Please choose one

Which bit of attributes byte represents the blue component of foreground color?

0

1

2

3

Question No: 17 (Marks: 1) - Please choose one

Which bit of the attributes byte represents the red component of background color ?

► 3

► 4

► 5

► 6

Question No: 18(Marks: 1) - Please choose one

INT 21 service 01H is used to read character from standard input with echo. It returns the result in _____ register.

► AL

► BL

► CL

► BH

Question No: 19 __ (Marks: 1) - Please choose one

A 32bit address register can access upto .....of memory so
Memory access has increased a lot.

1. 2GB

2. 4GB

3. 6GB

4. 8GB

Question No: 20 (Marks: 1) - Please choose one

Motorola 68K processors have ..... 23bit general purpose registers.

► 4

► 8

► 16

► 32

Question No: 21 __ (Marks: 1) - Please choose one

The table index is set ----to access GDT

- **0**
- 1
- -1
- -2

Question No: 22 __ (Marks: 1) - Please choose one
All the addressing modes in iAPX88 returns a number called ------addressing

- **Efective**
- Indirect
- Direct
- Faulty

Question No: 23 __ (Marks: 1) - Please choose one
In 9 pin DB connector ,which pic is assigned to RD.

- . 1
- **. 2**
- . 3
- . 4

Question No: 24 __ (Marks: 1) - Please choose one
In 9 pin DB connector ,which pic is assigned to DTR.

- . 1
- . 2
- . 3
- **. 4**

Question No: 25 __ (Marks: 1) - Please choose one
In 9 pin DB connector ,which pic is assigned to DSR.

- **. 6**
- . 5
- . 3
- . 4

Question No: 26 __ (Marks: 1) - Please choose one
. VESA organizes 16 bit color for every pixel in ratio
 5:5:5
 5:6:5
 6:5:6
 5:6:7

Question No: 27 __ (Marks: 1) - Please choose one
When an item is pushed on decrementing stack the top of the stack is

- First decrement then copied on to stack
- **First increment then copied on to stack**
- Increment after copied on to stack
- Decrement after copied on to stack

Question No: 28 __ (Marks: 1) - Please choose one
When an item is pushed on decrementing stack the top of the stack is

- First decrement then copied on to stack
- First increment then copied on to stack
- Increment after copied on to stack
- Decrement after copied on to stack

Question No: 29 __ (Marks: 1) - Please choose one

The space where all register of a task are stored is called

- Control Block
- **Process control block**
- Stack
- Memory

Question No: 30 __ (Marks: 1) - Please choose one

BIOS INT------ provide SERIAL port services

- **14**
- 13
- 11
- 12

Question No: 31 __ (Marks: 1) - Please choose one

To enable or disable interrupt port------

- **21**
- 20
- 60
- 61

Question No: 32 __ (Marks: 1) - Please choose one

Parallel port is called

- **DB-25**
- BD-25
- DB-24
- BD-24

Question No: 33 __ (Marks: 1) - Please choose one

386 processor is ---------- compatible to IAPX88

- **Down word**
- Up word
- Both
- None

Question No: 34 __ (Marks: 1) - Please choose one

Clearing stack in assembly by using

- Caller
- Callee
- **Ret**
- Stack

Question No: 35 __ (Marks: 1) - Please choose one

In 9 pin DB connector ,which pic is assigned to RI

- 6
- 7
- 8
- **9**

Question No: 35 __ (Marks: 1) - Please choose one

IDTR is ---Register

- 8 bit
- 16bit

- 32bit
- **48bit**

Question No: 36 __ (Marks: 1) - Please choose one
CALL instruction transfer control
- Permanently
- **Temporarily**
- Permanent when CALL is near
- Permanent when CALL is Far

Question No: 37 (Marks: 1) - Please choose one
Which bit of the attributes byte represents Green component of background color?
► 3
► 4
► 5
► 6

Question No: 38 (Marks: 1) - Please choose one
------- Pin (s) of processor is/are by ecternal hardware to generate INT
► 1
► 2
► 3
► 4

Question No: 42 (Marks: 2) -
INT 14 - SERIAL - READ CHARACTER 8 bit register return result in?
Question No: 43 (Marks: 2) -
What is the process control back answer in single line
Question No: 44 (Marks: 2) -
Explain Divide overflow
Question No: 45 (Marks: 2)
What is the system descriptor?

Question No: 46 (Marks:3)
It is the part of Multitasking TSR caller, what will do these instructions comment against them

```
Mov al, [chars+bx]
Mov [es:40],al
Inc bx
```

Question No: 47 (Marks:3)
INT10 VESA use which register to return result?
Question No: 48 (Marks:3)
Three basic steps B/w memory and processor to communicate .
Question No: 49 (Marks:3)
What is baud rate , tell the parity bit function.
Question No: 50 (Marks:5)
Write the instruction of following
Copy BL into CL

Copy DX into AX
Store 0x12 into AL
Store 0x1234 into AX
Store 0xFFFF into AX

Question No: 51 (Marks:5)
9 pin DB9 connector , write function of any five
Question No: 52 (Marks:5)
Fill in the blanks with proper words
AH = -09h --
AL = -- attribute --
BH = - page number ---
BL = - character to display ---
CX =--- number of times to write character --
(09h, page number, number of times to write character, attribute ,character to display,)
Question No: 52 (Marks:5)
Fill in the blanks with proper words
The GDT itself is an array of descriptors where each descriptor is an 8byte entry. The base and limit of GDT is stored in a 48bit register called the GDTR. This register is loaded with a special instruction LGDT and is given a memory address from where the 48bits are fetched.