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Final Paper
cs401 computer architecture and assembly language
(06/08/2010 9:30Am)

Short Questions

1. How many bytes floppy root directory entry has? (2)
2. How many calling conversion also tell the names? (2)
3. Which register is used as thread local variable? (2)
4. Write down the operations of CMP instruction? (2)
5. It is the part of Multitasking TSR caller, what will do these instructions comment against them (3)
   Mov al, [chars+bx]
   Mov [es:40],al
   Inc bx
6. Differentiate synchronous transmission and asynchronous transmission? (3)
7. List some architecture? (3)

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Subjective (every question has 5 marks)

1. What information is required to be provided for the service "INT14-SERIAL WRITE CHARACTER TO PORT" in the following registers?

AH=_____
AL=_____
DX=_____

2. Write into C language

```
[section.txt]
Global swap
swap:   mov ecx,[esp+4]     copy parameters p1 to ecx
        mov edx[esp+8]      copy parameters p2 to edx
        mov eax,[ecx]               copy *p1 to eax
        xchg eax,[edx]              exchange eax to *p2
        mov [ecx],eax               copy eax to *p1
        ret                 return
```

3. Which instruction makes trap flag zero? If there is not any then how we make it zero?